UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,107,434 B2 Page 1 of 1

APPLICATION NO.: 09/741616

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INVENTOR(S) : John et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8

Line 65, "Bit b0=0, Bit b1=0, ...and Bit b15=0" should read --... Bit b0=1, Bit b1=1, Bit b2=1, Bit b3=1, Bit b4=0, Bit b5=1, Bit b6=0, Bit b7=1, Bit b8=1, Bit b9=0, Bit b13=0, Bit b14=0, Bit b15=0...--.

Column 9

Line 29, "...by connecting by connecting..." should read --...by connecting...--.

Column 10

Line 52, "...Table II..." should read --...Table III...--.

Signed and Sealed this

Sixteenth Day of March, 2010

David J. Kappos

Director of the United States Patent and Trademark Office